



PATENT ABSTRACTS OF JAPAN

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(21) Application number: **05195739**(22) Date of filing: **06.08.1993**(71) Applicant: **MATSUSHITA ELECTRIC IND CO LTD**(72) Inventor: **KUROBE AKIO**(54) **RATE CONVERSION PICTURE CODER**

(57) Abstract:

PURPOSE: To set a frame rate and a quantization step to a visually optimum value at a slow communication line speed by synthesizing plural frame picture data received from a communication line whose communication speed is fast to be one-frame data.

CONSTITUTION: A signal is quantized by a 1st quantization means 109 and outputted to a communication line at a slow speed as $q(t(X_{i+n}-X_i)+Q'i+n)$. Simultaneously data are inversely quantized by a 2nd inverse quantization means, subject to inverse orthogonal transformation by a 2nd inverse orthogonal transformation means 111, the result is added to prediction data up to an i-th frame at an adder means 112 and the result is stored in a 2nd picture memory 113 as prediction data up to (i+n)th frame. Similarly the content of a 3rd picture memory is subject to inverse orthogonal transformation by a 1st inverse orthogonal transformation means 118 via a selector 105, the result is added to prediction data up to the i-th frame at an adder means 119 and stored in the 1st picture memory 120 as prediction data up to the (i+n)th frame.

When only a prediction error of the (i+1)th frame is a motion compensation prediction error, motion compensation is applied to the picture memories 120, 113.

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